

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet

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of

3

Complete if Known

Application Number	10/750,420
Filing Date	December 31, 2003
First Named Inventor	Yan Zhou
Group Art Unit	2616
Examiner Name	Chin, Wellington

Attorney Docket Number 75622P006301

U.S. PATENT DOCUMENTS

Examiner Initials ¹	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MMM-DD-YYYY	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
R		4,866,767	A	Tanimoto, et al.	Sep-12-1989	_____
R		5,329,588	A	Wilcox, et al.	Jul-23-1994	_____
R		5,452,345	A	Zhou, et al.	Sep-19-1995	_____
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R		6,226,331	B1	Gambuzza	May-01-2001	_____
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R		6,895,040	B2	Zhou	May-17-2005	_____
R		6,944,213	B2	Lee	Sep-13-2005	_____
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R		2002/0114444	A1	Anderson, et al.	Aug-22-2002	_____
R		2004/0124996	A1	Andersen	Jul-01-2004	_____
R		2004/0257114	A1	Hanneberg, et al.	Dec-23-2004	_____
R		2005/0147231	A1	Zhou	Jul-07-2005	_____
R		2005/0147232	A1	Zhou	Jul-07-2005	_____

FOREIGN PATENT DOCUMENTS

**Examiner
Signature**

Ram Singh

**Date
Considered**

5/21/07

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Attorney Docket Number **75622P006301**

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
RJ		INFINEON TECHNOLOGIES, "Product Brief: GEMINAX Global Enhanced Multiport Integrated ADSL Transceiver", Infineon Technologies AG, Germany (2001)	
RJ		INFINEON TECHNOLOGIES, "Product Brief: GEMINAX ADSL Transceiver Chipset", Infineon Technologies AG, Germany (2001)	
RJ		INFINEON TECHNOLOGIES, "Press Release: Infineon Technologies Introduces the IVAX Family - the First Chipset to Integrate Voice and Fullrate ADSL on a Single Line Card", Infineon Technologies AG, Germany (May 15, 2001)	
RJ		PESSL, PETER, ET AL. "A four channel Integrated Voice and ADSL Full-Rate Analog Front End", 27th European Solid-State Circuits Conference, Villach, Austria, September 18-20, 2001	

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